Question 1

What are the basic features of the U2000? Multiple Choices

Options:

A- Security management

B- Topology management

C- Alarm management

D- Performance management

Answer:

A, B, C, D

Question 2

The STAT indicator of the SDH board flashes red, lights up for 100ms, and turns off for 100ms. Which of the following means the state of the metric?

Options:

A- Hardware mismatch

B- Hardware failure

C- Normal operation

D- There is no power input

Answer:

A

Question 3

Question Type: MultipleChoice

EPL services use ports and bandwidth exclusively, and different users may need to share resources. Which of the following methods can be used to isolate user data?

A- VLAN ID

B- MPLS tags

C- QinQ Technical Wood

D- All of the above

Answer:

D

Question 4

Question Type: **MultipleChoice**

OTM nr.m consists of up to n " multiplexed channels and does not support non-channel-related overhead. The letter "r" indicates a decrease in functionality.

Options:

A- True

B- Error

Answer:

A

Question 5

Question Type: **MultipleChoice**

SL64 board is available for OSN 2500.

Options:

A- True

B- False

Answer:

B

Question 6

In the case of a 1+1 linear MSP, which of the following switching modes does not require the APS protection protocol full (multiple choice) to be enabled

Options:

A- Single-ended reply mode (correct case management).

B- Single-ended non-reply pattern

C- Double-ended reply mode

D- Double-ended non-reply mode

Answer:

A, B

Question 7

For alignment; Indicates the starting position of VC-n in the corresponding AU or TU frame. * This way: The receiver can correctly remove the corresponding VC from STM-N, and then The VC and C packages are separated into separate low-rate signals, such as PDH, which means that the low-rate path signal can be dropped directly from the STM-N signal.

Options:

A- True

B- Error

Answer:

A

Question 8

Question Type: **MultipleChoice**

Which of the following are device-level protection schemes for SDH products? (Multiple choice).

Options:

A- Power protection

B- Cross-connect and timer board protection

C- System dashboard protection

D- TPS protection

Answer:

A, B, C, D

Question 9

Question Type: MultipleChoice

For OSN3500, there is a slot mapping relationship between the processing board and the interface pre-processor. The processing board in slot 1 maps to the interface board in slots 19-20.

Options:

A- True

B- False

Question 10

Question Type: MultipleChoice

Which of the following statements about the EFS4 board are correct? (Multiple choice).

Options:

A- The board has four FE ports. .

B- The board supports three layers of text exchange. .

C- The plate must be used with the processing plate.

D- The board interface is the LC interface.

Answer:

A, B

Question 11

The scope of management of OMSn is the path from the input optical port on the multiplexer unit of the sending site to the output optical port on the multiplexer unit of the receiving site, called the optical multiplexing segment (OMS) path.

Options:

A- True

B- Error

Answer:

A